



VIDYA PRATISHTHAN'S KAMALNAYAN BAJAJ INSTITUTE OF ENGINEERING & TECHNOLOGY, BARAMATI

Department of Electronics & Telecommunication Engineering

Departmental Master Time Table 2024-25

SEM-I

(w.e.f - 1st July 2024)

Day	Time	9.15 To 10.15	10.15 To 11.15	11.15 To 11.30	11.30 To 12.30	12.30 To 01.30	1.30 To 2.45	02.45 To 03.45	03.45 To 04.45	04.45 To 05.45
	Class/ Period	1	2		3	4		5	6	7
Monday	S.Y.B.Tech	DLD-A1 (DE Lab) BHP A2-AEM (Tut) (RMTLab)SSK S&S-A3 (SSD Lab) JSR AC-A4 (COMM LAB) VUD			DLD - BHP (11B)	AC - VUD (11B)		S&S-JSR (11B)	AEM-SSK (11B)	NT-MSG (11B)
	T. E.	EM-(MMJ) (11B)	DBM-SRS (11B)		MC-A1 (SP Lab) JSR DC-A2 (COMM LAB) SBL Java-A3 (VLSI Lab) SDB DBM-A4 (SSD LAB) SRS			DC-(SBL) (12)	MC-(VUD) (12)	
	B. E. (A)		EL-II- SDB (12)		VLSI-VSS (12)	CC-RSP (12)		EL-I-A2 (SSD Lab) SDB CC- A3 (SP LAB) RSP VLSI-A4 (VLSI Lab) VSS RMT-A1 (RMT Lab) MMJ		
Tuesday	S.Y.B.Tech	DLD-A2 (DE Lab) BHP A3-AEM (Tut) (RMTLab)SSK S&S-A4 (SSD Lab) JSR AC-A1 (COMM LAB) VUD			DLD - BHP (11B)	AC - VUD (11B)		NT-MSG (11B)	AEM-SSK (11B)	HON/DM THEORY(11B)
	T. E.	EM-(MMJ) (11B)	DC-(SBL) (11B)		Java-A1 (VLSI Lab) MSG MC-A2 (SP Lab) JSR DC-A3 (COMM LAB) SBL SD-A4 (SSD LAB) RSP			MC-(VUD) (12)	Java-(MSG) (12)	
	B. E.(A)	VLSI-VSS (12)	EL-II- SDB (12)		EL-I-SDB (12)	RMT-MMJ (12)		RMT-A3 (RMT Lab) MMJ EL-I-A4 (SSD Lab) SDB VLSI-A2 (VLSI Lab) VSS CC- A1 (SP LAB) RSP		
Wednesday	S.Y.B.Tech	DLD-A3 (DE Lab) BHP A4-AEM (Tut) (RMTLab)SSK S&S-A1 (SSD Lab) JSR AC-A2 (COMM LAB) VUD			DLD - BHP (11B)	S&S-JSR (11B)		AC - VUD (11B)	NT-MSG (11B)	DT- RKS (11B) / ES - VSS (12)
	T. E.	EM-(MMJ) (11B)	DC-(SBL) (11B)		MC-A3 (SP Lab) VUD Java -A2 (VLSI Lab) MSG DC-A4 (COMM LAB) SBL DBM-A1 (SSD LAB) SRS			Java-(MSG) (12)	DBM-SRS (12)	
	B. E.(A)	CC-RSP (12)	EL-I-SDB (12)		RMT-MMJ (12)	VLSI-VSS (12)		EL-I-A3 (SSD Lab) SDB RMT-A2 (RMT Lab) MMJ CC- A4 (SP LAB) RSP VLSI-A1 (VLSI Lab) VSS		
Thursday	S.Y.B.Tech	DLD-A4 (DE Lab) BHP A1-AEM (Tut) (RMTLab)SSK S&S-A2 (SSD Lab) JSR AC-A3 (COMM LAB) VUD			AEM-SSK (11B)	S&S-JSR (11B)	Lunch Break	ES-A3 (SP LAB) T1 DT-A1(VLSI Lab) RKS		
	T. E.	DBM-SRS (11B)	Java-(MSG) (11B)		MC-A4 (SP Lab) VUD DBM-A2 (SSD LAB) SRS SD-A3-(VLSI LAB) RSP DC-A1 (COMM LAB) SBL			MC-(VUD) (12)	EM-(MMJ)TUT-A4 (12)	
	B. E.(A)	CC-RSP (12)	EL-I-SDB (12)		EL-II- SDB (12)	RMT-MMJ (12)		VLSI-A3 (SSD Lab) VSS RMT-A4 (RMT Lab) BHP Project Phase I-4		
Friday	S.Y.B.Tech	HON/DM THEORY (11B)	DT- RKS (11B) / ES - VSS (12)		ES-A2 (SP LAB) VSS DT-A4 (VLSI Lab) RKS			Hon VLSI- (VLSI Lab) T1 DM- (VLSI LAB) T2		
	T. E.	EM-(MMJ)TUT-A2 (12)			DBM-A3 (SSD LAB) SRS SD-A2-(COMM LAB) RSP EM-(MMJ)TUT 1 Hr- A1 (12)			Java-A4 (SSD Lab) SDB SD-A1-(SP LAB) RSP EM-(MMJ) TUT 1 Hr-A3 (12)		
	B. E.(A)	CC- A2 (SP LAB) RSP EL-I-A1 (SSD Lab) SDB Project Phase I- A1			Project Phase I- A2			Project Phase I-A3		
Saturday	S.Y.B.Tech	ES-A1 (SP LAB) VSS DT-A3 (VLSI Lab) RKS			ES-A4 (SP LAB) VSS DT-A2 (VLSI Lab) RKS			Hon VLSI- (VLSI Lab) T1 DM- (SP LAB) T2		
	T. E.									
	B. E.(A)									

Time-Table In-Charge
R.S. Piske

Time-Table Co-ordinator
M.D. Shelar

H.O.D.
Dr. B.H. Patil

Principal
Dr. R. S. Bichkar

S.Y.B.Tech
AEM :- Advance Engineering Mathematics- S.S.Khot
AC:- Analog Circuits :- V.U.Deshmukh
DLD:- Digital Logic Design :- Dr. B.H. Patil
DT :- Multidisciplinary Minor Course-Drone Technology :- Dr.R.K. Shastri
ES :- Multidisciplinary Minor Course Embedded System :-V.S.Surwase
NT-Network Theory:-M.S. Gawade
S&S:Signal &System :- Dr.J.S.Rangole
HON- Honor -Dr.R.K.Shastri/T1
DM- Double Minor-T2

TE A
MC:- Microcontrollers :- V.U.Deshmukh
DC:- Digital Communication :- Dr. S.B. Lande
DBM:- Data Base Management:-S.R.Sable
EM:-Electromagnetics :- M.M.Jadhav
SD: skill development: R.S.Piske
EL-I-(Java):-M.S. Gawade

BE A
VLSI : VLSI Design : V.S. Surwase
CC : Cloud computing: R.S.Piske
RMT:- Radiation & Microwave Techniques :- Dr. M.M. jadhav
EL-I: IOT :- S.D. Biradar
EL-II :- (EPD) :- S.D. Biradar